November 1989

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### COORDINATED SCIENCE LABORATORY

## FINAL REPORT OF RESEARCH



# FOR THE JOINT SERVICES ELECTRONICS PROGRAM

FOR THE PERIOD
OCTOBER 1, 1986, THROUGH SEPTEMBER 30, 1989
FOR
CONTRACT N00014-84-C-0149

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#### JSEP FINAL PROGRESS REPORT

For the Period

1 October 1986 through 30 September 1989

Joint Services Electronics Program Contract N00014-84-C-0149

Monitored by the Office of Naval Research

William Kenneth Jenkins
JSEP Principal Investigator
Coordinated Science Laboratory

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#### **EXECUTIVE SUMMARY**

#### Summary of the Program

This final report covers the Joint Services Electronics Program at the Coordinated Sciences Laboratory, University of Illinois at Urbana-Champaign, from October 1, 1986 through September 30, 1989. During this contract period, the program included twenty-three work units that covered five major technical areas: (1) Solid State Electronics, (2) Quantum Electronics, (3) Electromagnetics, (4) Information Systems, and (5) Electronics Research (Director's unit). There were seven units in Physical Electronics devoted mostly to the growth, characterization, and device fabrication of compound semiconductor materials. Quantum Electronics included three units: one unit studied charge density waves (DCWs) in superconducting materials, and two units dealt with plasmas and excited state chemistry of gases. Electromagnetics contained two units: one concentrated on monolithic millimeter-wave ICs with microstrip antennas and the second on the design and analysis of nonreflecting surface structures to control radar scattering characteristics. There were ten units in Information Systems covering important topics in computers, control, communications, and signal processing. Detailed discussions of individual accomplishments can be found in the "Annual Progress Report" covering each of years one, two, and three of the contract. During the three-year contract period, eight items were selected from among the many significant contributions and highlighted as "JSEP Most Significant Accomplishments." These accomplishments are summarized below.

#### JSEP MOST SIGNIFICANT ACCOMPLISHMENTS (October 1, 1986 - September 30, 1989)

#### The Heterostructure Hot Electron Diode (Unit 2 and Unit 5)

Professors J. J. Coleman and K. Hess, together with graduate students T. K. Higman and M. A. Emanuel, discovered a new switching mechanism in semiconductor heterostructures that resulted in the development of a new electronic device, the heterostructure hot electron diode (H<sup>2</sup>ED). The operation of the H<sup>2</sup>ED is based on a transition between two current conduction modes in a two-terminal structure, a low current tunneling mode, and a high current thermionic emission mode. Switching between these modes gives rise to an S-shaped negative differential resistance in the current versus voltage characteristic (as shown in Figure 1). These devices have application as a microwave oscillator. Microwave characterization is presently in progress, and preliminary results have demonstrated test-fixture-limited gain to 18 GHz. If no parasitic effects are encountered, and if the oscillation frequency is, in fact, electron transit time limited, oscillation in the 1006 GHz range may be possible.

This work represents a synergistic relationship between Professor Hess's theoretical work supported under Unit 2 and Professor Coleman's experimental work supported under Unit 5. Professor Hess developed the theoretical model of the new device and predicted its characteristics. Professor Coleman built the new device in his laboratory, measured the physical characteristics of his experimental sample, and is presently continuing its further characterization and development. It was announced in a press release by the University of Illinois at Urbana-Champaign to the UPI wire service that a patent disclosure has been filed on this new device, and a number of publications have appeared describing the technical details of the H<sup>2</sup>ED.

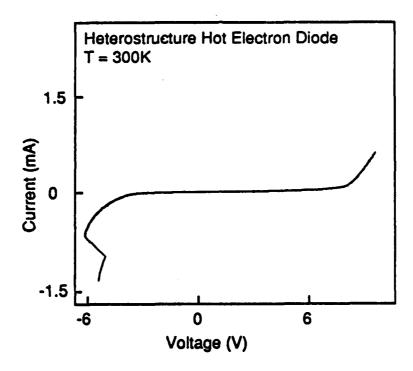


Fig. 1. Experimental room temperature dc I-V characteristics for the Heterostructure Hot Electron Diode (HHED) exhibiting the expected S-shaped NDR. The structure, grown on a (100) GaAs:Si n<sup>+</sup> substrate, consists of an undoped AlAs barrier (1500 Å), an undoped GaAs heating region (1300 Å), and a heavily doped n + Al<sub>0.20</sub>Ga<sub>0.80</sub>As cap layer. The theoretical maximum frequency of oscillation is estimated to be 57 GHz.

## Error-Control Coding and Spread-Spectrum Communication Techniques for the Army's SINCGARS Radio (Unit 21)

Several results of CSL research in error-control coding and spread-spectrum communications have been incorporated into the SINCGARS radio (Single Channel Ground/Airborne Radio Subsystem). This radio, the Army's newest tactical radio, was developed by ITT Aerospace/Optical Division for voice and data communications. It operates in both a single-channel mode and a frequency-hop spread-spectrum mode. In addition, an overlay is being developed by ITT and SRI International to enable SINCGARS radios to operate as packet radios in a mobile communication network.

Research results obtained by Professors M. B. Pursley and D. V. Sarwate and their graduate research assistants were employed by Professor Pursley to design the synchronization sequence that is utilized for timing acquisition in the SINCGARS receiver. In addition, for the packet radio version of SINCGARS (known as the SINCGARS Packet Overlay), Professor Pursley developed the error-control method that combines Reed-Solomon codes with side information derived from test patterns. The side information is used to erase unreliable data at the output of the demodulator so that the input to the decoder is a combination of data symbols and special erasure symbols. The code enables

the receiver to correct errors and erasures caused by noise and interference in the communication channel. The combination of coding and side information provides the capability for a large number of SINCGARS radios to operate simultaneously in the same frequency band. It also improves the radio's ability to communicate in the presence of various types of electromagnetic interference.

The decoding algorithm, developed and implemented in software by Professor Sarwate, is basically a mathematical description of the computations required to correct both errors and erasures. The actual computations prescribed by the algorithm are carried out in a microprocessor contained in the radio. The resulting decoder is capable of handling data rates in excess of 16,000 data symbols per second, the maximum signaling rate of the SINCGARS radio.

Professor Farsley also provided the analytical tools to evaluate the performance of the error-control scheme and assess its impact on the overall communication network performance. Analytical results from this effort, and other investigations conducted by Professor Pursley and Mr. J. R. McChesney of ITT, were used subsequently in the SINCGARS packet radio network simulation developed for the Army by SRI.

The research leading to the development of the approach used for error control in the SINCGARS radio was funded by JSEP during the early stages, funded by ARO during later stages, and developed into the actual SINCGARS application with funds provided by ITT and under individual consulting arrangements with SRI and ITT.

#### Design and Fabrication of a VLSI Digital Filter Integrated Circuit Module (Unit 17)

For a number of years, both NSF and JSEP have supported theoretical studies at the Coordinated Science Laboratory on the use of residue number arithmetic (RNS) for the design of highspeed VLSI circuits for the digital signal-processing functions that are required in modern communication and radar systems. Two years ago, Professors Jenkins and S. F. Lao carried out the successful design and fabrication of a semicustom VLSI digital filter module that demonstrates both the feasibility and desirability of using residue number theory design techniques in practice (Unit 17). A semicustom integrated circuit module was designed for an RNS digital filter using the IBM MVISA CAD system that is located on the campus of the University of Illinois at Urbana-Champaign. A programmable 4-bit module was designed as a basic building block for a finite impulse response digital filter that has order 8 and 14 bits of arithmetic precision. The module was designed using finite field logarithm additions to completely eliminate conventional multipliers on the chip and thereby improve speed and circuit density. The chip design was done with MVISA using a standard cell approach, and MVISA was then used to simulate the hardware operation and to estimate physical parameters. The chip was subsequently fabricated at the IBM Manassas facility, and 10 samples were eventually returned to the University of Illinois at Urbana-Champaign for testing and characterization. MVISA estimates that the 4.2 mm chip consumes 89 mW and operates at a system cycle frequency of 10 mHZ, which corresponds to a data-cycle frequency of 1.2 mHz. The design used 529 out of a possible 560 available standard cells. A photo of the IC layout is shown in Figure 2.

Experience gained in the above experiment led to a new integrated circuit design based on a modular systolic architecture. This new IC contains a fundamental computational element that is monitored internally on-chip by an error detection scheme. Many similar computational modules can be interconnected to form a wide variety of pipelined systolic arrays for implementing important computational kernels required in signal processing applications. At the time of this writing, a set of test chips has been received from MOSIS and these chips are now being tested at the University of Illinois.

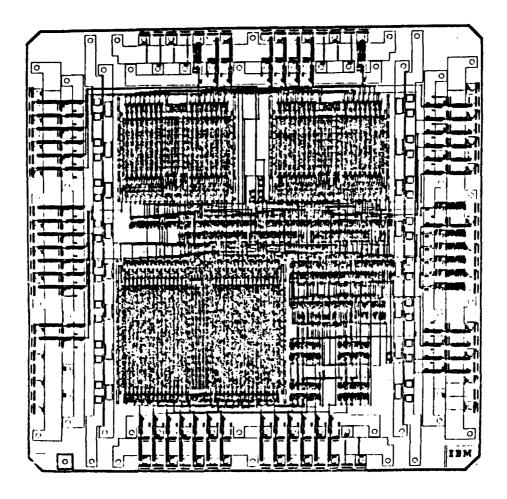


Fig. 2. A VLSI digital filter module designed with the MVISA CAD System.

Modeling and Simulation of New Microelectronic and Optoelectronic Devices and Circuits with the iSMILE Program (Unit 7)

During the last few years, Professors S. M. Kang and T. N. Trick, with graduate students K. Cioffi and A. T. Yang, collaborated on the development of circuit models for High Electron Mobility Transistors (HEMT) under the JSEP sponsorship. Traditionally, the task of developing new circuit models has been time consuming, mainly due to the intensive line-by-line modification of generic circuit simulators like SPICE or SLATE. As a result, the progress in computer-aided simulation of new microelectronic devices and circuits has been hampered by the slow model development process.

Under the JSEP sponsorship, Professor Kang and his Research Assistant A. T. Yang initiated the development of a new CAD tool, iSMILE (Illinois Simulator for Modeling of Integrated-circuit Level Elements). This program was first tried by Dr. Cioffi, now at Rockwell International, for his development of HEMT models. Circuit models are described to iSMILE by using a simple input file containing model topology descriptions and element characteristic equations. The iSMILE program then builds a circuit simulator based on circuit models, shielding the user from the laborious line-by-line program modifications. Owing to this unique feature, the model development time is reduced significantly from several months to a few weeks once the model developer has a clear understanding of underlying device physics.

Further development of the iSMILE program is continuing under the sponsorship of the National Science Foundation (NSF) Engineering Research Center for Compound Semiconductor Microelectronics. iSMILE has been used to develop lossy transmission models, multiple quantum well laser diode models, optical waveguide models, and photodetector circuit models in cooperation with Professors J. J. Coleman, T. A. DeTemple, and G. E. Stillman. Using these models and HEMT models, we have been able to simulate the speed performance of optical interconnect systems, which would not have been possible in such a short time without iSMILE.

The unique capability of the iSMILE program has been well recognized by industry collaborators. At their request, the iSMILE program has been ported to industrial researchers at Hewlett Packard, Boeing, GTE, AT&T, TI, and several universities. Results on circuit modeling and simulation were presented at the 1988 International Conference on Circuits and Systems and the 1988 International Conference on Computer Design. Further details of this work will be published in the 1989 Design Automation conference proceedings and journals. It was through the interdisciplinary team effort provided under the sponsorship of JSEP, NSF, and industry that enabled the development of new circuit models, as well as the novel CAD tool, iSMILE.

## Scanning Tunneling Microscopy of the CDW Discommensuration Domain Structure in the Nearly Commensurate Phase of 1T-TaS<sub>2</sub> (Unit 8)

It has been known for some time from bulk NMR and XPS measurements that the nearly commensurate CDW phases exhibited by some of the layered transition metal dichalcogenides actually consist of relatively small commensurate regions separated by CDW phase kinks or discommensurations (DCs). Their existence and microscopic domain structure have previously been predicted by the Ginzburg Landau approaches of McMillan and Nakanishi; however, no direct observation of the DC domain structure has been made until now.

Recently, Professor Joe Lyding and one of his students, Mr. Stephen Skala, have been able to use their new variable temperature scanning tunneling microscope (STM) (thermal drift < 1 Å /hour) to study the nearly commensurate phase of 1T-TaS<sub>2</sub>. Shown in Figure 3a is a representative STM image in which the larger period CDW is superimposed against the sulfur rows of the atomic lattice. From images like this, the commensurability of the CDW relative to the lattice can be studied. Shown in Figure 3b is a proposed model for the DC structure for Figure 3a and for 1T-TaS<sub>2</sub> in general. The small dots denote the sulfur atoms, the large dots are the CDW maxima, and the lines are the DCs between commensurate regions. Here it is seen that the domains are rhombohedral, with a characteristic length of 60 Å along each edge. The bold line to the right of Figure 3b represents a DC with a larger phase kink than the other DCs. The observed domain structure is not the hexagonal domain structure predicted by Nakanishi but one in which there are two equivalent CDW wavevectors (parallel to the DC boundaries), while the third wavevector passes through more DCs per unit length. This model also yields the correct average orientation of the incommensurate CDW wavevector relative to the lattice, as determined by bulk measurements.

#### Controlled Doping in MBE Si: Chemistry at the Atomic Level (Unit 1)

Fabrication of modern microelectronic devices requires precise control of dopant concentrations and depth distributions. Molecular beam epitaxy (MBE) is presently employed in many critical applications, such as the growth of superlattices and modulation-doped structures. However, most of the common dopants used in bulk Si wafers present serious problems during film growth by MBE due to low incorporation probabilities and/or pronounced surface segregation giving rise to uncontrolled profile broadening.

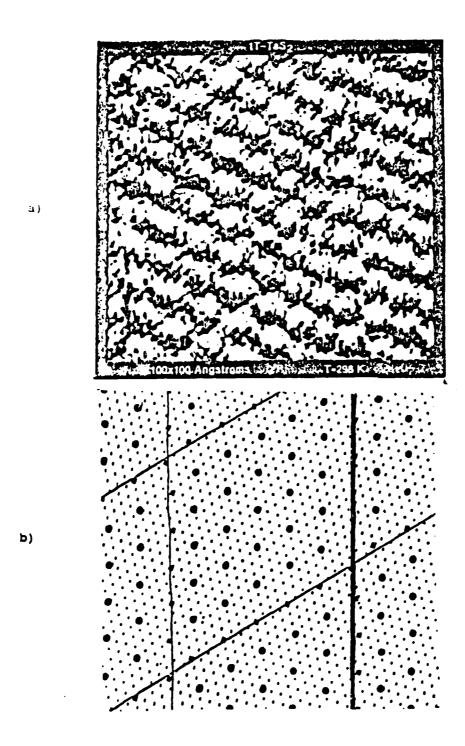


Fig. 3. a) STM image of the nearly commensurate phase of 1T-TaS<sub>2</sub> showing the orientation of the CDW (large period) relative to the atomic sulfur rows of the lattice. b) Discommensuration model constructed from a) where the small dots are atoms, large dots are CDW maxima, and the lines are discommensurations. The bold line is a discommensuration having a larger phase kink.

Under JSEP Unit 1, Professor Joe Greene has been investigating the use of low-energy (50-500 eV) accelerated-ion doping during MBE Si growth. The experiments are carried out using new ultra-high vacuum-compatible low-energy ion sources, which were developed as part of the research program. Sb, an important shallow donor in Si, and In, a deep acceptor, were chosen as model dopant materials.

Thermal Sb<sub>4</sub> and In, obtained from standard effusion cells, have low incorporation probabilities  $\sigma$  in MBE Si.  $\sigma_{\text{Sb}}$  ranges from  $10^{-3}$  to  $10^{-5}$  at growth temperatures  $T_s$  between 700 and 850 °C, while  $\sigma_{\text{In}}$  is <  $10^{-5}$  at  $T_s > 550$  °C. These dopants also exhibit severe surface segregation with, in the case of Sb, steady-state surface accumulations during growth of up to a full monolayer. This not only gives rise to broad doping profiles but also limits the maximum usable doping concentrations. For example, obtaining Sb concentrations higher than  $\approx 5 \times 10^{17} - 10^{18}$  cm<sup>-3</sup> with thermal-doping requires the use of extremely large Sb<sub>4</sub> fluxes, which results in the production of a high concentration of structural defects and a corresponding decrease in electron mobilities.

The University of Illinois at Urbana-Champaign Thin Film Physics Group is employing a combination of Auger electron spectroscopy (AES), low-energy and reflected high-energy electron diffraction (LEED and RHEED), plan-view and cross-sectional transmission electron microscopy (TEM and XTEM), secondary-ion mass spectrometry (SIMS), and temperature-dependent Hall measurements to investigate the growth, structure, dopant distribution, and electrical properties of Si films grown at  $T_s$ =800°C with 150 eV Sb<sup>+</sup> and 200 eV In<sup>+</sup> dopant beams. Sb and In were found to incorporate, with unity probability, into substitutional, electrically active sites at concentrations up to at least  $3x10^{19}$  cm<sup>-3</sup> and  $1x10^{18}$  cm<sup>-3</sup>, respectively. The films were essentially dislocation free with no indication of residual ion-induced damage. Data presented in Figure 4 show that carrier mobilities of Sb and In ion-doped films are equal to the best reported values for bulk Si even at doping concentrations extending well above those attainable by thermal-beam MBE. In fact, the hole mobilities are the highest ever reported for In-doped Si (note that the solid curve in Figure 4 corresponds to Si:B), whether bulk or thin film. Doping profiles in accelerated-beam modulation-doped structures were extremely abrupt. There was no indication from *in situ* AES and RHEED analyses of significant dopant surface accumulation during growth.

#### Conception and Demonstration of a New Mechanism of Tunnel Injection (Unit 24)

Professors Adesida, Kolodzey, and Leburton recently reported the conception and experimental verification of a new mechanism of tunnel injection into the active region of MODFET structures. Injection occurs through a tunnel junction parallel to the MODFET channel and is the basic mechanism for sophisticated high-speed multi-terminal devices. Two novel transistor structures were proposed—the BiTFET and the TIFET—that exploit the new injection principle in a configuration leading to Negative Differential Resistance (NDR) characteristics. Theoretical estimates of the relevant time constants indicate possible operation in the 100 GHz range.

With barrior height equal to the band gap, the use of tunnel junctions has the advantage of reducing thermionic emission competing with the tunneling current and results in high peak-to-valley NDR ratios that are suitable for high-power microwave devices.

TIFET structures have been fabricated and tested in our laboratory. Experimental I-V characteristics show evidence of tunneling in the 2-D channel of the MODFET across the tunnel junction (see Figure 5). A patent application for the new devices has been filed by University Patents, Inc.

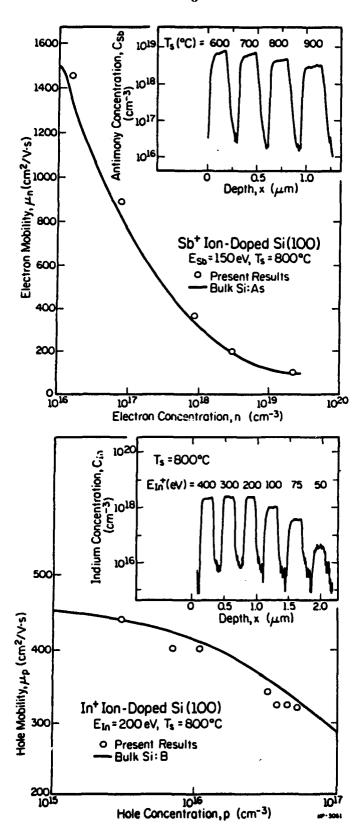


Fig. 4. Experimental carrier mobilities of Sb and In ion-doped films.

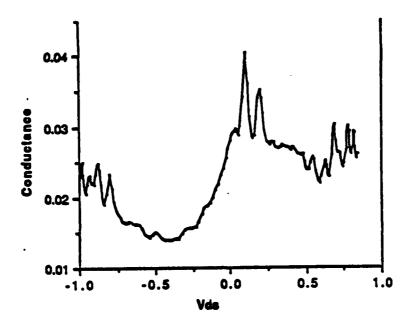


Fig. 5. Conductance vs.  $V_{ds}$  for GaAs/AlGaAs TIFET Device,  $I_{sb} = +5mA$ ,  $V_{gs} = 1V$ 

#### Completion of the University of Illinois EpiCenter (Unit 23)

In late 1989 construction of the University of Illinois EpiCenter was completed and researchers are now successfully growing materials. The EpiCenter is a world-class facility that consists of seven MBE chambers interconnected by high-vacuum transfer tubes. Having the chambers, each of which is dedicated to a different type of material growth and characterization, interconnected by vacuum lines allows samples to be moved from one growth environment to another without external contamination. Since the planning for this new facility was begun about five years ago and the construction has taken two years, the completion of this project is a major accomplishment. All of the JSEP discretionary funds provided under Unit 23 of the 86-89 contract were applied toward the purchase of several MBE chambers that will support future MBE research in the JSEP program. Since the EpiCenter is described in detail in the Annual Progress Report for year three of the contract, further details will not be presented in this report.

#### LIST OF INVESTIGATORS

## Principal Investigator William Kenneth Jenkins

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Jacob A. Abraham Ilesanmi Adesida Erdal Arikan Prithviraj Banerjee Andrew R. Barron Tamer Basar Donna Brown Shun-Lien Chuang James J. Coleman Jose B. Cruz, Jr. J. Gary Eden W. Kent Fuchs Joseph E. Greene Jessy W. Grizzle Bruce E. Hajek Ibrahim N. Haji Karl Hess Thomas S. Huang Ravi K. Iver William Kenneth Jenkins Sung Mo Kang Arun S. Karalamangala (a.k.a. K. S. Arun) Miles V. Klein Petar V. Kokotovic

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## JSEP-SUPPORTED DEGREES AWARDED October 1, 1986, through September 30, 1989

#### April 1, 1986 - March 31, 1987

- [1] A. Ali, Graduate Student, "Time-domain reflectometry using scattering parameters and a deembedding application," M.S. thesis, 1986.
- [2] D. W. Bailey, Research Assistant, "Estimators in Monte Carlo semiconductor electron transport simulations," M.S. thesis, 1986.
- [3] R. Bansal, Research Assistant, "Solutions to some stochastic control problems with nonclassical information," M.S. thesis, 1986.
- [4] C. A. Cavins, "Electron-polar optical phonon scattering in one-dimensional multilevel semiconductor structures," M.S. thesis, 1986.
- [5] T. Cwik, Graduate Student, "Scattering from general periodic screens," Ph.D. dissertation, 1986.
- [6] R. DeJule, Research Assistant, "Study of deep levels in (100) and (311) B molecular beam epitaxial gallium arsenide by constant capacitance deep-level transient spectroscopy," Ph.D. dissertation, Sept. 1986.
- [7] R. Hall, Graduate Student, "Electromagnetic scattering from periodic structures comprised of resistive sheet material," Ph.D. dissertation, 1986.
- [8] K. B. Kahen, Ph.D., "Theoretical study of the transverse dielectric constant of superlattices and their alloys," Ph.D. dissertation, 1986.
- [9] S. F. Lao, Research Assistant, "VLSI implementation of a residue number digital filter," M.S. thesis, 1986.
- [10] A. J. Levitan, M.S., "Static and dynamical properties of doping (N-I-P-I) superlattices," M.S. thesis, 1986.
- [11] D. Lubben, Research Assistant, "Photon-stimulated surface reactions in laser-induced and laser-assisted crystal growth," Ph.D. dissertation, 1986.
- [12] M. Malkawi, Graduate Research Assistant, "Compiler directed memory management for numerical programs," Ph.D. dissertation, 1986.
- [13] P. A. Martin, "Deep-level transient spectroscopy studies of gallium arsenide-aluminum gallium arsenide heterostructures and superlattices," Ph.D. dissertation, 1986.
- [14] M. L. Prastein, Research Assistant, "Precedence-constrained scheduling with minimum time and communication," M.S. thesis, Jan. 1987.
- [15] A. Rockett, Research Assistant, "Impurity incorporation and redistribution during the growth of single-crystal semiconductors by molecular beam epitaxy: A theoretical and experimental study," Ph.D. dissertation, 1986.

- [16] H. S. Tharp, Research Assistant, "Frequency-weighted projective controls for large scale system design," Ph.D. dissertation, 1986.
- [17] T. Wang, Research Assistant, "Theoretical studies of electronic transport in gallium arsenide material and devices using an ensemble Monte Carlo method," Ph.D. dissertation, 1986.
- [18] A. T. Yang, Research Assistant, "Delay modeling of bipolar ECL/EFL circuits," M.S. thesis, 1986.

#### April 1, 1987 - March 31, 1988

- [1] K. Beuscher, Research Assistant, "Representation, analysis, and design of multirate discrete-time control systems," M.S. thesis, Feb. 1988.
- [2] R. L. Cruz, "A calculus for network delay and a note on topologies of interconnection networks, Ph.D. thesis, July 1987.
- [3] M. A. Emanuel, Research Assistant, "Metalorganic chemical vapor deposition and its application to the growth of the heterostructure hot electron diode," Ph.D. thesis, May 1988.
- [4] J. S. Gerstenberger, "Improving timing estimation accuracy through the use of register transfer level descriptions," M.S. thesis, Dec. 1987.
- [5] R. Gerth, Research Assistant, "Numerical computation of maximal controlled-invariant subspaces and maximal controlled invariant distributions," M.S. thesis, May 1987.
- [6] M. A. Haase, Research Assistant, "Internal photo emission and energy band discontinuities at semiconductor heterojunctions," Ph.D. thesis, Dec. 1987.
- [7] G. A. Hebner, "Measurement of the radiation temperature and electron density in parallel plate radio frequency discharges," Ph.D. thesis, Oct. 1987.
- [8] M. C. Hsueh, Research Assistant and Visiting Professor, "Measurement-based reliability / performability models," Ph.D. thesis, Sept. 1987.
- [9] S. Laha, Research Assistant, "Accurate low-cost methods for performance evaluation of cache memory systems," Ph.D. thesis, Nov. 1987.
- [10] F. Lai, Research Assistant, "Rule-based circuit optimization for CMOS VLSI," Ph.D. thesis, 1987.
- [11] U. Madhow, "Bounds and asymptotic results for the performance of asynchronous frequency-hop packet radio networks," M.S. thesis, June 1987.
- [12] S. Manion, Research Assistant, "Two-dimensional electron gases in high magnetic fields," Ph.D. thesis, Jan. 1988.
- [13] J. J. Murphy, "A comparison of transform domain adaptive filters, with emphasis on the Hartley transform," M.S. thesis, Aug. 1987.

- [14] N. Pan, Research Assistant, "Hydrogenation of high purity gallium arsenide and liquid phase epitaxy growth of high purity indium gallium arsenide," Ph.D. thesis, Dec. 1987.
- [15] R. Ramaker, Research Assistant, "Design of low order controllers for robust disturbance rejection in large space structures," M.S. thesis, Sept. 1987.
- [16] V. M. Robbins, Research Assistant, "An experimental study of impact ionization in compound semiconductors," Ph.D. thesis, Jan. 1988.
- [17] G. Sasaki, "Optimization by simulated annealing: a time complexity analysis," Ph.D. thesis, Oct. 1987.
- [18] T. Ting, Research Assistant, "Robustness in feedback systems," Ph.D. thesis, June 1987.

#### April 1, 1988 - March 31, 1989

- [1] M. A. Aboelaze, Research Assistant, "Systematic design of computational arrays," Ph.D. thesis, Apr. 1988.
- [2] K. M. Baumgartner, Graduate Research Assistant, "Resource allocation on distributed computer systems," Ph.D. thesis, May 1988.
- [3] S. S. Bose, Research Assistant, "Low temperature magneto-photoluminescence characterization of high purity gallium arsenide and indium phosphide," Ph.D. thesis, 1988.
- [4] A. Chang, Graduate Student, "Numerical methods to solve the problem of scattering from electrically large bodies," Ph.D. thesis, 1988.
- [5] D. P. Connors, Research Assistant, "Balance of recurrence order in time-inhomogeneous Markov chains with application to simulated annealing," Ph.D. thesis, Apr. 1988.
- [6] M. A. Haase, Research Assistant, "Internal photoemission and energy band discontinuities at semiconductor heterojunctions," Ph.D. thesis, May 1988.
- [7] J. Higman, Research Assistant, "Theoretical studies of high field electron transport in silicon devices," Ph.D. thesis, 1988.
- [8] T. K. Higman, Research Assistant, "Experimental investigation of hot electron and related effects in gallium (aluminum) arsenide devices," Ph.D. thesis, 1988.
- [9] A. Kanevsky, Research Assistant, "Vertex connectivity of graphs: algorithms and bounds," Ph.D. thesis, 1988.
- [10] K. W. Kim, Research Assistant, "Monte Carlo studies of nonlinear electron transport in III-V semiconductors," Ph.D. thesis, 1988.
- [11] M. H. Kim, Research Assistant, "Photo-Hall studies of compound semiconductors," Ph.D. thesis, 1988.

- [12] I. C. Kizilyalli, Research Assistant, "Physics and simulation of high-speed heterostructure devices," Ph.D. thesis, 1988.
- [13] B. Lee, Research Assistant, "Characterization of shallow impurities in high-purity gallium arsenide and indium phosphide using photothermal spectroscopy: nonequilibrium incorporation of amphoteric impurities," Ph.D. thesis, Apr. 1989.
- [14] S.-F. Lin, Research Assistant, "Performance and robustness of adaptive controllers for linear stochastic systems," Ph.D. thesis, 1988.
- [15] W. G. Lyons, Research Assistant, "Transport studies and phenomenological model for sliding charge density waves in quasi one-dimensional conductors," Ph.D. thesis, Jan. 1989.
- [16] S. J. Manion, Research Assistant, "Acoustic phonon scattering in modulation doped aluminum<sub>x</sub> gallium<sub>1-x</sub> arsenide/gallium arsenide heterojunctions," Ph.D. thesis, 1988.
- [17] N. Pan, Research Assistant, "Hydrogenation of high purity gallium arsenide and liquid phase epitaxy growth of high purity indium gallium arsenide," Ph.D. thesis, 1988.
- [18] A. Reed, Research Assistant, "Residual impurity incorporation in the growth of high purity gallium arsenide by metalorganic chemical vapor deposition," Ph.D. thesis, 1988.
- [19] D. W. Smart, Research Assistant, "Parallel processing techniques for the simulation of MOS VLSI circuits using waveform relaxation," Ph.D. thesis, 1988.
- [20] R. Tamassia, Research Assistant, "Dynamic data structures for two-dimensional searching," Ph.D. thesis, 1988.
- [21] J. L. Trahan, Research Assistant, "Instruction sets for parallel random access machines," Ph.D. thesis, 1988.
- [22] H.-C. C. Tseng, "Internal manifold in system design with application to flexible link robot control," Ph.D. thesis, June 1988.
- [23] D. R. Wagner, Research Assistant, "A survey of high-speed digital filter structures and their finite precision behavior," M.S. thesis, May 1988.

#### **PUBLICATIONS BY WORK UNITS**

#### **WORK UNIT NUMBER 1**

TITLE: Crystal Growth from the Vapor Phase and Controlled Doping of Equilibrium and Metastable Semiconductor Alloys: Ion-Surface Interactions

#### SENIOR PRINCIPAL INVESTIGATOR:

J. E. Greene, Research Professor

- [1] S. A. Barnett, H. F. Winters, and J. E. Greene, "The interaction of Sb<sub>4</sub> molecular beams with Si(100) surfaces: Modulated-beam mass spectrometry and thermally stimulated desorption studies," Surf. Sci., vol. 165, pp. 303-326, 1986. (JSEP/SRC)
- [2] J. Knall, J.-E. Sundgren, G. V. Hansson, and J. E. Greene, "In overlayers on clean Si(100)2x1: Surface structure, nucleation, and growth," *Surf. Sci.*, vol. 166, pp. 512-538, 1986. (JSEP/SRC)
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- [4] A. Rockett, J. Klem, S. A. Barnett, J. F. Greene, and H. Morkoc, "Si incorporation and segregation on  $Ga_{1-x}Al_xAs$  (100) films grown by molecular beam epitaxy," J. Vac. Sci. Technol. B, vol. 4, pp. 519-520, 1986. (JSEP)
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- [6] T. C. McGlinn, T. N. Krabach, M. V. Klein, G. Bajor, J. E. Greene, B. Kramer, S. A. Barnett, A. Lastras, and S. Gorbatkin, "Raman scattering and optical absorption studies of the metastable alloy system GaAs<sub>1-x</sub>Sb<sub>x</sub>," *Phys. Rev. B*, vol. 33, pp. 8396-8401, 1986. (JSEP)
- [7] J. E. Greene, T. Motooka, J.-E. Sundgren, A. Rockett, S. Gorbatkin, D. Lubben, and S. A. Barnett, "A review of the present understanding of the role of ion/surface interactions and photo-induced reactions during vapor phase crystal growth," J. Cryst. Growth, vol. 79, pp. 19-32, 1986. (JSEP/DOE/SRC)
- [8] M. A. Hasan, J. Knall, S. A. Barnett, J.-E. Sundgren, A. Rockett, and J. E. Greene, "Accelerated-ion beam doping during Si growth by molecular beam epitaxy and ion-enhanced In film deposition using low energy (40 to 300 eV) In<sup>+</sup> ion source," *Vacuum 36*, pp. 1017-1018, 1986. (JSEP/SRC)

- [9] S. A. Barnett, H. F. Winters, and J. E. Greene, "Influence of Ion bombardment on the interaction of Sb with the Si(100) surface," Surf. Sci., vol. 181, pp. 596-603, 1987. (JSEP/SRC)
- [10] S. I. Shah, J. E. Greene, L. L. Abels, Q. Yao, and P. M. Raccah, "Growth of single-crystal metastable Ge<sub>1-x</sub>Sn<sub>x</sub> alloys on Ge(100) and GaAs(100) substrates," *J. Crystal Growth*, vol. 83, pp. 3-10, 1987. (JSEP/DOE)
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TITLE: Studies of Transport Phenomena in Semiconductors

#### **SENIOR PRINCIPAL INVESTIGATORS:**

K. Hess, Research Professor

J. P. Leburton, Research Associate Professor (participated during year one)

- [1] K. Hess, T. K. Higman, M. A. Emanuel, and J. J. Coleman, "New ultrafast switching mechanism in semiconductor heterostructures," *J. Appl. Phys.*, vol. 60, no. 10, pp. 3775-3777, 1986. (JSEP/ONR/ARO)
- [2] J. Higman and K. Hess, "Comment on the use of the electron temperature concept for nonlinear transport problems in semiconductor p-n junctions," *Solid-State Electronics*, vol. 29, no. 9, pp. 915-918, 1986. (JSEP)
- [3] I. C. Kizilyalli, K. Hess, G. J. Iafrate, and D. Smith, "Dynamics of electron transfer between two adjacent channels as calculated by an ensemble Monte Carlo method," *Proc. NUMOS Workshop*, Los Angeles, CA, Dec. 1986. (ARO/JSEP)
- [4] J. P. Leburton, "Anomalous current oscillations in semiconductor-insulator-semiconductor structures and related devices," NATO Summer School, Antwerp, Belgium, 1986. (JSEP)
- [5] J. P. Leburton, "Microscopic interpretation of the current oscillations in GaAs<sup>+</sup> -AlGaAs-GaAs<sup>-</sup> tunnel junctions," *Proc. 18th I.C.P.S.*, Stockholm, Sweden, 1986, pp. 1623-1626. (JSEP)
- [6] S. L. Chuang and K. Hess, "Tunneling-assisted impact ionization for a superlattice," J. Appl. Phys. vol. 61, no. 4, pp. 1510-1515, Feb. 1987. (NASA/JSEP)
- [7] J. P. Leburton, "Space charge generation by LO phonon ionization of neutral donors in GaAs-AlGaAs-GaAs tunnel junctions," *Bull. Am. Phys. Soc.*, vol. 32, pp. 886, 1987. (JSEP)
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- [9] I. C. Kizilyalli and K. Hess, "Electron transfer between adjacent channels simulated by ensemble Monte Carlo methods," J. Appl. Phys., vol. 61, no. 6, pp. 2395-2398, 1987. (ARO/JSEP)
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- [15] I. C. Kizilyalli, K. Hess, T. Higman, M. Emanuel, and J. J. Coleman, "Ensemble Monte Carlo Simulation of Real Space Transfer (NERFET/CHINT) Devices," *Solid-State Electron.*, vol. 31, no. 3/4, pp. 355-357, 1988. (ARO/JSEP)
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TITLE: Basic Studies of the Optical and Electronic Properties of Defects and Impurities in Compound Semiconductor Epitaxial Layers and Related Superlattices

#### SENIOR PRINCIPAL INVESTIGATOR:

G. E. Stillman, Research Professor

- [1] N. Pan, B. Lee, S. S. Bose, M. H. Kim, J. S. Hughes, G. E. Stillman, and K. Arai, "Si donor neutralization in high purity GaAs," *Appl. Phys. Lett.*, vol. 50, pp. 1832-1834, June 22, 1987. (NSF/JSEP)
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- [3] S. S. Bose, M. H. Kim, and G. E. Stillman, "Identification of residual donors in high purity undoped p-type epitaxial GaAs by magneto-photoluminescence," *Appl. Phys. Lett.*, vol. 53, pp. 980-982, Sept. 1988. (DARPA/AFOSR/JSEP/NSF-CDR)
- [4] B. Lee, M. H. Kim, S. S. Bose, G. E. Stillman, E. C. Larkins, E. S. Hellman, D. G. Schlom, and J. S. Harris, Jr., "Sulfur incorporation in undoped high purity n-type GaAs grown by molecular beam epitaxy," in *Proc. Int. Symp. on Gallium Arsenide and Related Compounds, Inst. Phys. Conf. Ser. No. 96*, Atlanta, GA, Sept. 1988, Chapter 2, pp. 23-28. (NSF-CDR/JSEP/Kodak/DARPA/ONR)
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TITLE: Heterostructure Electronic Devices by Metalorganic Chemical Vapor Deposition (MOCVD)

#### SENIOR PRINCIPAL INVESTIGATOR:

J. J. Coleman, Research Professor

- [1] T. K. Higman, M. A. Emanuel, J. J. Coleman, S. J. Jeng, and C. M. Wayman, "Structural analysis of Au-Ni-Ge and Au-Ag-Ge alloyed ohmic contacts on modulation doped AlGaAs-GaAs heterostructures," J. Appl. Phys., vol. 60, p. 677, 1986. (JSEP/NSF)
- [2] P. A. Martin, K. Hess, M. A. Emanuel, and J. J. Coleman, "Deep level transient spectroscopy studies of defects in GaAs AlGaAs superlattices," J. Appl. Phys., vol. 60, p. 2882, 1986. (JSEP/ONR)
- [3] K. Hess, T. K. Higman, M. A. Emanuel, and J. J. Coleman, "New ultrafast switching mechanism in semiconductor heterostructures," J. Appl. Phys., vol. 60, p. 3755, 1986. (JSEP/ONR/ARO)
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TITLE: Optical Properties of MBE-Grown Structures

#### SENIOR PRINCIPAL INVESTIGATORS:

M. V. Klein, Research Professor

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TITLE: Computer-Aided Design of High-Performance Integrated Circuits with Ultra-Small Features

#### **SENIOR PRINCIPAL INVESTIGATORS:**

- I. N. Hajj, Research Professor
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- V. B. Rao, Research Assistant Professor

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## WORK UNIT NUMBER 24 (Unit began 2nd reporting period)

TITLE: Electronic and Transport Properties of Ultra-Low-Dimensional Structures

#### **SENIOR INVESTIGATORS:**

- J. P. Leburton, Research Associate Professor
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TITLE: Collective Electronic Transport in Quasi One-Dimensional Systems

#### SENIOR PRINCIPAL INVESTIGATORS:

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TITLE: An Investigation of Plasma and Chemistry Processes in Cylindrical Magnetron Plasma Discharges

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**TITLE:** Excited State Chemistry in Gases

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#### SENIOR PRINCIPAL INVESTIGATORS:

S. L. Chuang, Research Associate Professor Y. T. Lo, Research Professor

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R. Mittra, Research Professor

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#### SENIOR PRINCIPAL INVESTIGATORS:

- J. A. Abraham, Research Professor
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# **SENIOR PRINCIPAL INVESTIGATORS:**

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## **SENIOR PRINCIPAL INVESTIGATORS:**

- D. J. Brown, Research Associate Professor
- M. C. Loui, Research Associate Professor
- F. P. Preparata, Research Professor
- V. L. Ramachandran, Research Assistant Professor

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- T. S. Huang, Research Professor
- W. K. Jenkins, Research Professor
- D. C. Munson, Research Professor

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TITLE: Parallel VLSI Structures for Sensor Array Processing

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TITLE: Adaptive Algorithms for Identification, Filtering, Control, and Signal Processing

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TITLE: Distributed and Decentralized Systems

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TITLE: Robust Feedback Control of Nonlinear Systems

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TITLE: Multiple-Terminal Digital Communication Systems

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TITLE: Basic Research in Electronics

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The objective of this unit is to provide discretionary funds to the Director for support of new initiatives on basic problems of electronic materials, devices, and systems in a timely manner and to provide early start-up funding of projects that present immediate opportunities of high scientific promise. These discretionary funds are an important feature of the JSEP program in that they support exploratory work on new topics, provide matching equipment funds in the laboratory, and support promising work of new faculty where appropriate.

In late 1989 construction of the University of Illinois EpiCenter was completed and researchers are now successfully growing materials. The EpiCenter is a world-class facility that consists of seven MBE chambers interconnected by high-vacuum transfer tubes. Having the chambers, each of which is dedicated to a different type of material growth and characterization, interconnected by vacuum lines allows samples to be moved from one growth environment to another without external contamination. Since the planning for this new facility was begun about five years ago and the construction has taken two years, the completion of this project is a major accomplishment. All of the JSEP discretionary funds provided under Unit 23 of the 86-89 contract were applied toward the purchase of several MBE chambers that will support future MBE research in the JSEP program. Since the EpiCenter is described in detail in the Annual Progress Report for year three of the contract, further details will not be presented in this report.